

	U	1	Document ID	Issue Date	Pages
1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030112029 A1	20030619	11
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030084370 A1	20030501	21
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020018376 A1	20020214	5
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20010007141 A1	20010705	20
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6601205 B1	20030729	9
6	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6543016 B1	20030401	23
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6442073 B1	20020827	20
8	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6438048 B1	20020820	13
9	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6405324 B2	20020611	18
10	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6356491 B1	20020312	12
11	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6340588 B1	20020122	117
12	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6329139 B1	20011211	194

	Title	Current OR	Current XRef
1	Built-in self repair for an integrated circuit	326/10	
2	Circuit and method for masking a dormant memory cell	714/8	714/710
3	Method and a device for testing electronic memory devices	365/200	
4	Circuit and method for masking a dormant memory cell	714/718	
5	Method to descramble the data mapping in memory circuits	714/741	345/551; 714/720; 714/728
6	Testing content-addressable memories	714/718	711/108; 714/703
7	Nonvolatile memory cell with multiple gate oxide thicknesses	365/185.25	365/185.26; 365/185.29; 365/185.3
8	Nonvolatile memory and high speed memory test method	365/201	365/185.09; 714/42; 714/732
9	Circuit and method for masking a dormant memory cell	714/8	714/11
10	Method and circuit for rapidly equilibrating paired digit lines of a memory device during testing	365/201	365/202; 365/230.06
11	Matrices with memories	435/287.1	435/287.2; 435/288.1; 435/288.3; 435/288.4; 435/288.7; 530/300; 530/334; 530/350; 536/23.1; 536/24.3; 536/25.3
12	Automated sorting system for matrices with memory	435/6	209/597; 209/604; 702/19; 702/20

	Retrieval Classif	Inventor	S	C	P	2	3	4	5
1		Zimlich, David A.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2		Shore, Michael	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3		Campardo, Giovanni et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4		Shore, Michael	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5		Lehmann, Gunther et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6		Lewandowski, James L. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7		Sansbury, James D.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8		Kumar, Promod	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
9		Shore, Michael	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
10		Mullarkey, Patrick J. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
11		Nova, Michael P. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
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11	US 6340588	<input type="checkbox"/>
12	US 6329139	<input type="checkbox"/>

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13	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6319668 B1	20011120	91
14	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6284459 B1	20010904	96
15	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6282134 B1	20010828	12
16	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6282122 B1	20010828	20
17	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6236597 B1	20010522	20
18	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6195762 B1	20010227	19
19	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6137737 A	20001024	13

	Title	Current OR	Current XRef
13	Method for tagging and screening molecules	435/6	365/183; 435/287.2; 435/288.7; 435/7.1; 530/333; 530/334; 536/24.3; 536/25.3; 711/1
14	Solid support matrices with memories and combinatorial libraries therefrom	435/6	422/68.1; 435/287.1; 435/287.2; 435/288.1; 435/288.3; 435/288.4; 435/7.1; 436/501; 436/519; 436/520; 436/527; 436/533; 436/534; 436/535; 530/350; 530/354
15	Memory test method and nonvolatile memory with low error masking probability	365/201	714/25; 714/37; 714/39; 714/45; 714/48; 714/724; 714/726; 714/732; 714/733
16	Evaluation of memory cell characteristics	365/185.24	365/185.01; 365/185.02; 365/185.14; 365/185.18
17	Nonvolatile memory cell with multiple gate oxide thicknesses	365/185.28	365/185.01; 365/185.22; 365/185.26
18	Circuit and method for masking a dormant memory cell	714/8	714/711
19	Method and circuit for rapidly equilibrating paired digit lines of a memory device during testing	365/201	365/205; 365/206; 365/230.06

	Retrieval Classif	Inventor	S	C	P	2	3	4	5
13		Nova, Michael P. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
14		Nova, Michael P. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
15		Kumar, Promod	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
16		Sansbury, James D.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
17		Sansbury, James D.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
18		Shore, Michael	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
19		Mullarkey, Patrick J. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

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15	US 6282134	<input type="checkbox"/>
16	US 6282122	<input type="checkbox"/>
17	US 6236597	<input type="checkbox"/>
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19	US 6137737	<input type="checkbox"/>



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20	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6100026 A	20000808	89
21	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6037799 A	20000314	23
22	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6031763 A	20000229	20
23	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6026505 A	20000215	18
24	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6025129 A	20000215	67
25	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6017496 A	20000125	113

	Title	Current OR	Current XRef
20	Matrices with memories and uses thereof	435/6	422/119; 422/58; 422/68.1; 435/287.1; 435/287.2; 435/288.1; 435/288.3; 435/7.1; 435/7.2; 435/7.3; 435/7.4
21	Circuit and method for selecting a signal	326/37	327/407; 327/525
22	Evaluation of memory cell characteristics	365/185.24	365/185.01; 365/185.14; 365/185.18; 365/185.21
23	Method and apparatus for real time two dimensional redundancy allocation	714/711	714/718; 714/719
24	Remotely programmable matrices with memories and uses thereof	435/6	235/439; 250/328; 435/7.1; 435/7.2; 435/7.4; 435/7.8; 435/7.92; 435/91.1; 435/91.2; 530/333; 536/24.3; 536/24.33
25	Matrices with memories and uses thereof	422/68.1	422/102; 422/104; 422/107; 422/108; 422/50; 422/58; 422/99; 435/6; 435/7.1; 702/22

	Retrieval Classif	Inventor	S	C	P	2	3	4	5
20		Nova, Michael P. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
21		McClure, David C.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
22		Sansbury, James D.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
23		Hedberg, Erik Leigh et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
24		Nova, Michael P. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
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20	US 6100026	<input type="checkbox"/>
21	US 6037799	<input type="checkbox"/>
22	US 6031763	<input type="checkbox"/>
23	US 6026505	<input type="checkbox"/>
24	US 6025129	<input type="checkbox"/>
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26	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5961923 A	19991005	119
27	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5959891 A	19990928	20
28	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5859804 A	19990112	18
29	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5841709 A	19981124	23
30	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5771195 A	19980623	23
31	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5768206 A	19980616	7
32	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5732033 A	19980324	13
33	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5592427 A	19970107	33
34	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5566386 A	19961015	33
35	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5506807 A	19960409	12

	Title	Current OR	Current XRef
26	Matrices with memories and uses thereof	422/68.1	422/102; 422/104; 422/55; 422/57; 422/58; 422/82.05; 436/164; 436/165; 436/518; 436/524; 436/528; 436/531; 702/150; 702/19; 702/28
27	Evaluation of memory cell characteristics	365/185.24	365/185.21; 365/185.29; 365/185.3; 365/218
28	Method and apparatus for real time two dimensional redundancy allocation	365/201	365/200; 714/718
29	Memory having and method for testing redundant memory cells	365/200	365/201; 365/225.7; 365/230.06
30	Circuit and method for replacing a defective memory cell with a redundant memory cell	365/200	365/225.7; 365/230.06
31	Circuit and method for biasing bit lines	365/225.7	365/189.11; 365/200
32	Method and circuit for rapidly equilibrating paired digit lines of a memory device during testing	365/201	365/200; 365/230.06
33	Semiconductor memory having a sense amplifier with load transistors having different load characteristics	365/205	327/51; 365/185.21
34	Nonvolatile semiconductor memory device having a status register and test method for the same	365/226	365/201
35	Memory circuit with redundancy	365/200	365/225.7; 365/230.02; 714/710

	Retrieval Classif	Inventor	S	C	P	2	3	4	5
26		Nova, Michael P. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
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29		McClure, David C.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
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32		Mullarkey, Patrick J. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
33		Kumakura, Sinsuke et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
34		Kumakura, Sinsuke et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
35		Ferrant, Richard et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

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